

X-1495 US  
10/676,929

PATENT  
Conf. No.:7851



IN THE UNITED STATES PATENT OFFICE

Applicant: Amit Singh  
Assignee: Xilinx, Inc.  
Title: Circuit Clustering During Placement  
Serial No.: 10/676,929 File Date: September 30, 2003  
Examiner: Tuyen P.To Art Unit: 2825  
Docket No.: X-1495 US Conf. No.: 7851

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on March 23, 2006, please replace/substitute the following claims as indicated.

**Amendments to the claims** are reflected in the list of claims that begins on page 2 of this paper.

**Remarks** begin on page 8 of this paper.

IT IS OK TO ENTER ,

TT 05/26/2006